

### **Abstract**

Systems and methods are provided for latching a data signal. In one embodiment, a system comprises a first delay circuit that programmably delays a strobe signal with a first delay to latch a data signal. The system also comprises a second delay circuit that receives the data signal and delays the data signal with a second delay that is substantially inherent to the first delay. The system may include a logic circuit coupled between the first and the second delay circuits for latching the data signal in substantial alignment with the strobe signal. In one embodiment, similar delays are used in a master delay circuit, while in another embodiment such delays are used in slave devices connected to a master delay circuit.